

WHAT IS CLAIMED IS:

Sub A1 } 1. A method of erasing and/or programming data and/or programs in a memory arrangement of a computer, comprising the steps of:

providing an identifier into an area of the memory arrangement that is to be erased and/or programmed, the identifier identifying a correct erasing and/or programming of the memory arrangement; and

altering the identifier in the memory arrangement before erasing and/or programming the data and/or the programs.

Sub C1 } 2. The method according to Claim 1, wherein the computer is a control unit in a motor vehicle.

Sub B1 } 3. The method according to Claim 1, wherein the altering step includes the substep of: altering the identifier by erasing and/or programming.

4. The method according to Claim 1, further comprising the step of: entering the identifier into a further area of the memory arrangement, the further area being erased and/or programmed only after erasing and/or programming of the area.

5. The method according to Claim 4, wherein the further area is to be erased and/or programmed last.

Sub A2 } 6. The method according to Claim 1, wherein the identifier is at least one of a component of the data and a component of the programs.

Sub B1 } 7. The method according to Claim 1, further comprising the step of: altering the identifier by erasing and/or programming so that the identifier is unidentifiable.

Sub A3 } 8. The method according to Claim 1, wherein the identifier is a section of a program identifier which identifies the respective data and/or the programs.

Sub B1
9. The method according to Claim 1, further comprising the step of:
checking the identifier after at least one of (a) an interruption in erasing and/or programming
and (b) erasing and/or programming the memory arrangement.

Sub C1
10. The method according to Claim 9, further comprising the step of:
storing the interruption with a flag in the memory arrangement.

Sub B1
11. The method according to Claim 10, further comprising the steps of:
checking at least one of the identifier and the flag before erasing and/or programming; and
analyzing at least one of the identifier and the flag before erasing and/or programming.

Sub A4
12. A method of reprogramming data and/or programs in a memory arrangement of a
computer, comprising the step of:
selecting an identifier from the data and/or the programs entered into an area of the memory
to be erased and/or programmed, the identifier identifying a correct erasing and/or programming of
the memory arrangement.

Sub C1
13. The method according to Claim 12, wherein the computer is a control unit in a motor
vehicle.

Sub A5
14. The method according to Claim 12, further comprising the step of:
selecting the identifier from the data and/or the programs entered into a further area of the
memory arrangement, the further area being erased and/or programmed only after erasing and/or
programming of the area.

Sub B1
15. The method according to Claim 14, wherein the further area is to be erased and/or
programmed last.

Sub A6
16. The method according to Claim 12, further comprising the step of:

Ad6 altering the selected identifier in the memory arrangement before erasing and/or programming the data and/or the programs.

Sub B1 17. The method according to Claim 16, wherein the altering step includes the substep of: altering the selected identifier by erasing and/or programming.

Sub A7 18. The method according to Claim 12, further comprising the step of: selecting the identifier as at least one section of a predetermined length of the data and/or the programs entered into the memory arrangement.

Sub B1 19. The method according to Claim 12, further comprising the step of: altering the identifier by erasing and/or programming so that the identifier is unidentifiable.

Sub A8 20. The method according to Claim 12, wherein the identifier is a section of a program identifier which identifies the data and/or the programs.

Sub B1 21. The method according to Claim 12, further comprising the step of: checking the identifier after at least one of (a) an interruption in erasing and/or programming and (b) erasing and/or programming the memory arrangement.

Sub C1 22. The method according to Claim 21, further comprising the step of: storing the interruption with a flag in the memory arrangement.

Sub B1 23. The method according to Claim 22, further comprising the steps of: checking at least one of the identifier and the flag before erasing and/or programming; and analyzing at least one of the identifier and the flag before erasing and/or programming.

Sub A9 24. A device for erasing and/or programming data and/or programs in a memory arrangement of a computer, comprising:

A9

- Sub
C1

- Sub B1

- Sub A10

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- [Signature]*

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